

1 **ABSTRACT**

2 Described herein is a system for operating memory at reduced power, based
3 on whether the memory is actually in use. A memory controller receives
4 notifications from an operating system or other computer program regarding
5 which areas of physical memory are actually in use. The memory controller is
6 responsive to the notifications to operate unused portions of memory at reduced
7 power. In systems having refreshable memory, the memory controller omits
8 refreshing for those memory rows that are not currently in use.

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